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**PALM INTRANET****Inventor Information for 10/033513**

| Inventor Name    | City   | State/Country |
|------------------|--------|---------------|
| TAIPALE, DANA J. | AUSTIN | TEXAS         |
| KOIRALA, DIPESH  | AUSTIN | TEXAS         |

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Last Name = TAIPALE

First Name = DANA

| Application#             | Patent#    | Status | Date Filed | Title   | Inventor Name 7    |
|--------------------------|------------|--------|------------|---|--------------------|
| <a href="#">10131662</a> | Not Issued | 061    | 04/24/2002 | METHOD AND APPARATUS FOR DETERMINING AN UPPER DATA RATE FOR A VARIABLE DATA RATE SIGNAL | TAIPALE, DANA      |
| <a href="#">10033513</a> | Not Issued | 077    | 12/26/2001 | POST-CORRELATION INTERPOLATION FOR DELAY LOCKED LOOPS                                   | TAIPALE, DANA J.   |
| <a href="#">09499402</a> | 6477679    | 150    | 02/07/2000 | METHODS FOR DECODING DATA IN DIGITAL COMMUNICATION SYSTEMS                              | TAIPALE, DANA J.   |
| <a href="#">09498852</a> | 6477681    | 150    | 02/07/2000 | METHODS FOR DECODING DATA IN DIGITAL COMMUNICATION SYSTEMS                              | TAIPALE, DANA J..  |
| <a href="#">09131213</a> | 6310856    | 150    | 08/07/1998 | CDMA COMMUNICATIONS SYSTEM HAVING A SEARCHER RECEIVER AND METHOD THEREFOR               | TAIPALE, DANA JOHN |
| <a href="#">08867657</a> | Not Issued | 164    | 06/02/1997 | SYMMETRICAL AUDIO EQUALIZER AND METHOD THEREFOR   | TAIPALE, DANA      |

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Last Name = KOIRALA

First Name = DIPESH

| Application#             | Patent#    | Status | Date Filed | Title   | Inventor Name 3 |
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| <a href="#">10131662</a> | Not Issued | 061    | 04/24/2002 | METHOD AND APPARATUS FOR DETERMINING AN UPPER DATA RATE FOR A VARIABLE DATA RATE SIGNAL | KOIRALA, DIPESH |
| <a href="#">10033513</a> | Not Issued | 077    | 12/26/2001 | POST-CORRELATION INTERPOLATION FOR DELAY LOCKED LOOPS                                   | KOIRALA, DIPESH |

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[PDF] [A Low-Power DS-CDMA RAKE Receiver Utilizing Resource Allocation ...](#)

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The output of the **interpolator** is then fed directly to an **early late correlator** to generate an error signal. This signal ...

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The **early late correlator** generates a timing error ... The output of the **interpolator** is then fed directly to an **early late correlator** to generate an error ...

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On-Time. DPCCH. Early/Late. DPDCH. Control. **Correlator** pool. Beam former ...

**Interpolator**. Filter. DPDCH. Correlation. PN Code. Generator ...

[www.coe.montana.edu/ee/rwolff/EE548/sring05%20papers/Freescale%20adaptive%20antenna%20paper.pdf](http://www.coe.montana.edu/ee/rwolff/EE548/sring05%20papers/Freescale%20adaptive%20antenna%20paper.pdf) - [Similar pages](#)

[PDF] [A reconfigurable 100 Mchip/s spread spectrum receiver - Acoustics ...](#)

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the classic **early late** gate tracking 'S-curve'. A ... channel **on-time** received samples is performed using a serial **correlator** started at the assumed symbol ...

[ieeexplore.ieee.org/iel5/8535/27072/01202397.pdf?arnumber=1202397](http://ieeexplore.ieee.org/iel5/8535/27072/01202397.pdf?arnumber=1202397) - [Similar pages](#)

[PDF] [CDMA RECEIVER DESIGN ASPECTS](#)

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Early-late spacing of 0.25 chips. 83080 Receiver Architectures and Signal ...

**Interpolator**. (higher order => higher resolution). rx sign. **Correlator** ...

[www.cs.tut.fi/kurssit/83080/CDMA.pdf](http://www.cs.tut.fi/kurssit/83080/CDMA.pdf) - [Similar pages](#)

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complies with the well-known **early-late** gate algorithm in Figure 9, ... chip timing resolution, we introduce a linear **interpolator** into our system. ...

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Nonlinearity. Nonlinearity. +. -. Late. Early. On-Time ... **Correlator**. **Correlator**.

Reference Access Probe Generator. Reference Access Probe Generator ...

[www.xilinx.com/esp/wireless/collateral/CDMA2000\\_esp.pdf](http://www.xilinx.com/esp/wireless/collateral/CDMA2000_esp.pdf) - [Similar pages](#)

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Page 1. 2-1 TM File Number 4868.1 CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures. ...

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[PDF] [HFA3861B](#)

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Page 1. TM 1 File Number 4816 HFA3861B Direct Sequence Spread Spectrum Baseband Processor The Intersil HFA3861B Direct Sequence Spread ...

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EARLY. ON-TIME. LATE. CORRELATION TIME. FIGURE 17. CORRELATION PROCESS ...

The symbol clock is tracked by a sample **interpolator** that ...

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Page 1. 1 @ FN8019.2 CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures. 1-888 ...

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Layout of the 9 channel TDC. Integral nonlinearity of the 512 **interpolator** channels.

... in heart rate variability analysis: Effects of editing **on time** ...

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### [PDF] IST-1999-10025, WIND-FLEX D4.7 Synchronization issues for single ...

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... 2). ...23 Figure 5. Basic principle of the differential **correlator** using the cyclic prefix. An asterisk ...

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### EUSIPCO-96 CD-ROM Proceedings (abstracts)

Using these pulses, combination of matched filter and **interpolator** for ...

It is based on a generalized cross-**correlator** and an improved peak detector. ...

[www.eurasip.org/content/mediaassets/pdf/abstract/all.htm](http://www.eurasip.org/content/mediaassets/pdf/abstract/all.htm) - 513k - [Cached](#) - [Similar pages](#)

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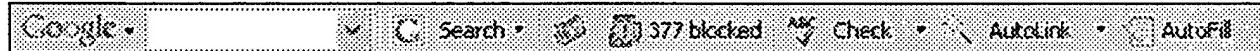
The maximum time span prevents the **interpolator** from using data across a too ...

[www.forsbergservices.co.uk/Files/product\\_manuals/RTKNav3.14.pdf](http://www.forsbergservices.co.uk/Files/product_manuals/RTKNav3.14.pdf) - [Similar pages](#)

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...87 4.1.4 Timing **Interpolator**...36 FIGURE 21. **EARLY LATE** STRUCTURE...37 FIGURE 22. S CURVE FOR **EARLY LATE**...

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Feb 2005

...of a bank of programmable **correlators** that correlate the incoming...Presenting the correct data to the **correlators** at the right time is a challenging...timing recovery or data **correlator**. The PUs share access to...raised cosine filters and **interpolator**.

3. The combining unit performs...

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1. **Decision-directed coherent delay-lock-tracking loop for DS-spread-spectrum signals**

de Gaudenzi, R.; Luise, M.;  
Communications, IEEE Transactions on  
Volume 39, Issue 5, May 1991 Page(s):758 - 765

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2. **Spread-spectrum ranging multipath model validation**

Braasch, M.S.; DiBenedetto, M.F.;  
Aerospace and Electronic Systems, IEEE Transactions on  
Volume 37, Issue 1, Jan 2001 Page(s):298 - 304

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3. **From matched filters to martingales**

Kailath, T.;  
Information Theory, 1998. Proceedings. 1998 IEEE International Symposium on  
16-21 Aug. 1998 Page(s):2

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4. **A combined coherent carrier recovery and decision-directed delay-lock-loop scheme for DS/SSMA communication systems employing complex spreading sequences**

Marx, F.E.; Linde, L.P.;  
Spread Spectrum Techniques and Applications, 1998. Proceedings., 1998 IEEE 5th International Symposium on  
Volume 3, 2-4 Sept. 1998 Page(s):837 - 842 vol.3

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5. **A non-coherent tracking scheme for the RAKE receiver that can cope with unresolvable multipath**

Aue, V.; Fettweis, G.P.;  
Communications, 1999. ICC '99. 1999 IEEE International Conference on  
Volume 3, 6-10 June 1999 Page(s):1917 - 1921 vol.3

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6. **Comparison of PN code tracking digital DLL's for direct sequence spread spectrum system**

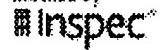
Maljevic, I.; Sousa, E.S.;  
Personal, Indoor and Mobile Radio Communications, 2004. PIMRC 2004. 15th IEEE International Symposium on  
Volume 4, 5-8 Sept. 2004 Page(s):2944 - 2948 Vol.4

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| L13 | 3   | interpolat\$3 with first with second with third with correlator           | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
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| L18 | 7    | interpolat\$3 with correlator with sample WITH CONTROL   | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/06/09 10:52 |
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| L22 | 3841 | delay adj locked adj loop  | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/06/09 10:52 |
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| L24 | 90   | interpolat\$3 and correlator and early and late and (delay adj locked adj loop) and (post adje correlator) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/06/09 10:52 |
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|-----|-------|--|---|----|----|------------------|
| L27 | 1     | "09/760094"  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L28 | 17620 | early with late with from adn<br>(delay adj locked adj loop) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L29 | 0     | early with late with from and<br>(delay adj locked adj loop) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L30 | 0     | early with late with from                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L31 | 33007 | early with late  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L32 | 3841  | delay adj locked adj loop                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L33 | 88    | L31 with L32   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L34 | 396   | L31 and L32  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |

|     |      |  |   |    |    |                  |
|-----|------|--|---|----|----|------------------|
| L35 | 0    | early with late with(from)                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L36 | 0    | early with late with (from)                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L37 | 1209 | early with late with using                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L38 | 121  | L37 and L32  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L39 | 28   | L37 with L32   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L40 | 20   | interpolat\$3 same correlator same early same late             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L41 | 1    | interpolat\$3 same correlator same early same late same ontine | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L42 | 2    | interpolat\$3 and correlator and (early with late) and ontine  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |

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|-----|------|---|---|----|----|------------------|
| L43 | 1301 | 375/150   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L44 | 41   | L38 and L43   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L45 | 47   | correlator and (early with late)<br>and interpolator  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L46 | 15   | L32 and L45   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L47 | 221  | correlator and (early with late)<br>and interpolat\$3 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L48 | 88   | L32 and L47   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L49 | 45   | L47 and L43   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L50 | 26   | L48 and L43   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |

|     |      |             |   |    |    |                  |
|-----|------|-------------|---|----|----|------------------|
| L51 | 431  | 375/136     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L52 | 4    | L48 and L51 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L53 | 901  | 375/142     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L54 | 18   | L48 and L53 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L55 | 1353 | 375/147     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L56 | 19   | L48 and L55 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L57 | 1450 | 375/343     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L58 | 11   | L48 and L57 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |

|     |      |             |   |    |    |                  |
|-----|------|-------------|---|----|----|------------------|
| L59 | 1420 | 375/373     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L60 | 1    | L48 and L59 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L61 | 3350 | 375/376     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |
| L62 | 1    | L48 and L61 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/06/09 10:52 |